

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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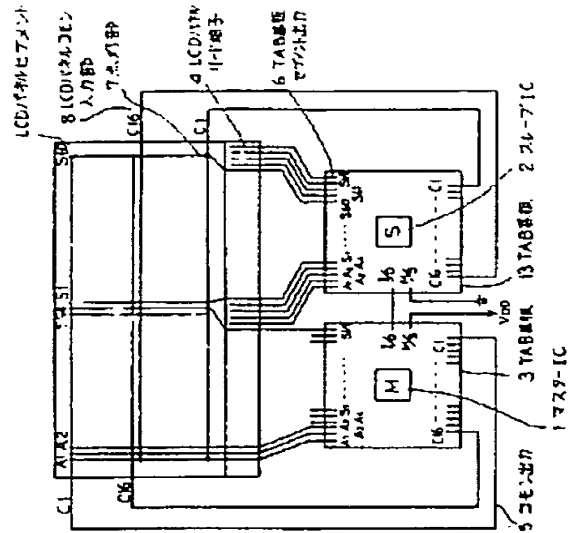
APPLICATION DATE : 14-05-90  
APPLICATION NUMBER : 02124684

APPLICANT : SEIKO INSTR INC;

INVENTOR : CHIBA MITSUYOSHI;

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TITLE : MOUNTING STRUCTURE OF LCD  
MODULE



ABSTRACT : PURPOSE: To make it possible to use plural TAB substrates having the same shape and the same pattern layout for a master IC circuit substrate and a slave IC circuit substrate in common even if the number of terminals of one substrate is different from that of the other by mounting a master IC and a slave IC respectively on the TAB substrates.

CONSTITUTION: The master IC 1 and the slave IC 2 for driving an LCD panel are mounted on respective TAB substrates 3, 13 having the same shape and the same pattern layout. Respective ICs 1, 2 are electrically connected to the inner lead parts of the TAB substrates 3, 13 by using a thermocompression bonding means or the like. Common output terminals from the TAB substrates 3, 13 are connected to the common input part 8 of the LCD panel through a flexible substrate or the like by the thermocompression bonding means to form the display of a dot matrix display lighting part 7. Thus, the two TAB substrates 3, 13 can be used in common.

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